

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<p>In re: Application of: Carballo et al.</p> <p>Serial No.: 10/255,471</p> <p>Filed: 09/26/2002</p> <p>Title: METHOD OF TRANSPARENTLY REDUCING POWER CONSUMPTION OF A HIGH-SPEED COMMUNICATION LINK</p>	<p>Before the Examiner: Chang, Daniel D.</p> <p>Group Art Unit: 2819</p>
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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

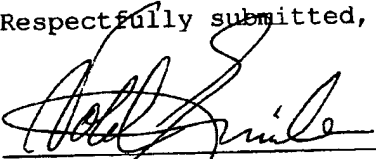
Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56 (a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 CFR 1.98 (a)(1).

Respectfully submitted,



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In Place of FORM PTO-1449 (Modified)

Serial No. 10/255,471

Applicants: Carballo et al.

**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT**

Filing Date: 09/26/2002

Group: 2819

Atty. Docket No. AUS920020446US1

Reference Designation

U. S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	AA 6,122,336	9/19/00	Anderson	375	371	9/11/97
	AB 6,107,946	8/22/00	Jeong	341	101	2/3/98
	AC					
	AD					
	AE					
	AF					

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
	AL					
	AM					
	AN					
	AO					
	AP					

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner
Initial

AR Wei-Hung Chen, Guang-Kaai Dehng, Jong-Woei Chen and Shen-Iuan Liu, A CMOS 400-MB/s Serial Link for AS-Memory Systems Using a PWM Scheme, IEEE Journal of Solid State Circuits, Vol. 36, Issue 10, Oct. 2001 pp. 1498 - 1505.

AS Chih-Kong Ken Yang, Ramin Farjad-Rad and Mark A. Horowitz, A 0.5- μ m CMOS 4.0-Gbit/s Serial Link Transceiver With Data Recovery Using Oversampling, IEEE Journal of Solid State Circuits, Vol. 33, Issue 5, May 1998, pp. 713-722.

AT John M. Khoury and Kadaba R. Lakshmikumar, High-Speed Serial Transceivers for Data Communication Systems, IEEE Communications Magazine, Vol. 39, Issue 7, July 2001, pp. 160-165

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.